

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

QI XIANG

Serial No.: Divisional of  
Application No. 10/015,808

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Group Art Unit: To be assigned

Filed: July 17, 2003

Examiner: To be assigned

For: CMOS WITH STRAINED SILICON CHANNEL NMOS AND SILICON GERMANIUM  
CHANNEL PMOS

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop NEW APPLICATIONS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

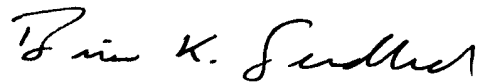
The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/015,808 , filed December 17, 2001 , which is relied upon for an

earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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**Date: July 17, 2003**

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)				ATTY. DOCKET NO. <b>64965-168</b>		SERIAL NO. <b>Divisional of Serial No. 10/015,808</b>	
				APPLICANT <b>QI XIANG</b>			
				FILING DATE <b>July 17, 2003</b>		GROUP <b>To be assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Document Number <small>Number-Kind Code<sup>2</sup> (if known)</small>	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US 20020123167A1	09/2002	Fitzgerald			
		US 6,407,406 B1	7/2002	Tezuka			
		US 6,214,653 B1	04/2001	Chen et al			
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<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document <small>Country Codes-Number + -Kind Codes (if known)</small>	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		Shallow Trench Isolation, "Trench Isolation," <a href="http://courses.nus.edu.sg/course/phy/&gt;pgs. 1-4">http://courses.nus.edu.sg/course/phy/&gt;pgs. 1-4</a>					
		Hitachi America, Ltd., Semiconductor Equipment Group, "Customizable Shallow Trench Isolation," <a href="http://www.hitachi.com/semiequipment/sti.html/&gt;pg 1">http://www.hitachi.com/semiequipment/sti.html/&gt;pg 1</a>					
		SNP Applications/Shallow Trench Isolation (STI), "Shallow Trench Isolation (STD)," <a href="http://www.surfaceinterface.com/snpappsSTI.html/&gt;pgs 1-2">http://www.surfaceinterface.com/snpappsSTI.html/&gt;pgs 1-2</a>					
		Institute of Microelectronics - Deep Submicron - Shallow Trench Isolation, "Shallow Trench Isolation Module Developmetn", <a href="http://www.ime.org.sg/deep_trench.htm/&gt;pgs. 1-2">http://www.ime.org.sg/deep_trench.htm/&gt;pgs. 1-2</a>					
EXAMINER				DATE CONSIDERED			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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		US					
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		US					
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<b>FOREIGN PATENT DOCUMENTS</b>							
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						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		David Lammers, "MIT spinout preps commercial strained silicon", Oct. 22, 2001, <a href="http://www.eetimes.com/story/OEG20011022S0078/">http://www.eetimes.com/story/OEG20011022S0078/</a> , pgs. 1-5					
		IBM's Strained Silicon Breakthrough Image Page, June 8, 2001, <a href="http://www.research.ibm.com/resources/press/strainedsilicon/">http://www.research.ibm.com/resources/press/strainedsilicon/</a> , pgs. 1-2					
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		Matthew French, "Amber Wave Systems 'strained silicon' significant for semiconductor industry", August 6, 2001, <a href="http://www.mass.../displaydetail.asp?/">http://www.mass.../displaydetail.asp?/</a> , pgs. 1-3					
		Richard Ball, "Strained silicon wafers boost FET speed 80 per cent at US start-up", Electronics Weekly Archive, pg. 1					
		Orla Higgins, Press Release, "Amber Wave Systems Corporation Announces Availability of Breakthrough Strained Silicon Technology", October 22, 2001, pgs. 1-4					
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EXAMINER			DATE CONSIDERED				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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